Solving large sparse linear systems and least squares

Miroslav Tůma

Faculty of Mathematics and Physics Charles University

mirektuma@karlin.mff.cuni.cz

SNA'25, Ostrava, January 2025

Outline



The main text resources are

- Jennifer Scott and Miroslav Tůma: Algorithms for sparse linear systems, Birkhäuser- Springer, 2023, open access.
- Jennifer Scott and Miroslav Tůma: Solving large sparse linear least squares, Acta Numerica, 2025, to appear.
- Further resources mentioned in these two texts

Our two problems

• Systems of linear algebraic equations

$$\mathbf{A}\mathbf{x} = \mathbf{b}, A \in \mathbb{R}^{n \times n}$$

Our two problems

• Systems of linear algebraic equations

$$\mathbf{A}\mathbf{x} = \mathbf{b}, A \in \mathbb{R}^{n \times n}$$

Solving the (overdetermined) linear least squares (LS) problems

Given $A \in \mathbb{R}^{m \times n}$ of rank $n, m \ge n$ and $b \in \mathbb{R}^m$

find $x \in \mathbb{R}^n$ that minimises $||b - Ax||_2$.

Theorem

x is a solution of this least squares (LS) problem $\iff x$ satisfies the $n \times n$ normal equations

$$Cx = A^T b, \quad C = A^T A$$

LS → one linear system. Enables to discuss LE and LS jointly

LE: two principially different classes of solution methods

- I. Direct methods: heirs of Gaussian elimination, formulated as

 a) factorization, b) solution by substitution steps
 - ▶ a) Cholesky $A \rightarrow LL^T$ (A SPD), $A \rightarrow LU$ (A factorizable), indefinite factorizations, QR.
 - b) the factorized matrix used to find the solution (by substitution)
- An example: Ax = b: A = LU, $y = L^{-1}b$, $x = U^{-1}y$

LE: two principially different classes of solution methods

- I. Direct methods: heirs of Gaussian elimination, formulated as

 a) factorization, b) solution by substitution steps
 - ▶ a) Cholesky $A \rightarrow LL^T$ (A SPD), $A \rightarrow LU$ (A factorizable), indefinite factorizations, QR.
 - b) the factorized matrix used to find the solution (by substitution)
- An example: Ax = b: A = LU, $y = L^{-1}b$, $x = U^{-1}y$
- II. Iterative methods
- Compute a sequence of approximations $x^{(0)}, x^{(1)}, x^{(2)}, \ldots$ that hopefully converges to the solution x of the linear system.
- Various approaches
 - Stationary iterative methods (linearly convergent)
 - Krylov subspace methods (typically more efficient)
 - (Some) convergence theory for both classes of methods

Direct methods and iterative methods once more

Direct methods

- Designed to solve the systems of equations.
- Properly implemented: they are robust, often predictable accuracy.
- They can be expensive, requiring large amounts of memory.

Iterative methods

- Designed to approximate (not solve)
- This may be an advantage if only an approximate solution is needed
- Can be terminated as soon as the required accuracy is achieved
- But this may be also a disadvantage (if matrix properties prohibit achieving the required accuracy, stopping iterations)

- First idea: operation counts
- In direct methods it seems to us that most of the work is in the factorization, less in the substitution steps.
 - Fully populated A: n^2 entries
 - ★ $1/3n^3 + O(n^2)$ complexity of Cholesky
 - * $2/3n^3 + O(n^2)$ complexity of LU (factorizable A)
 - ★ Substitution steps: only $O(n^2)$

- First idea: operation counts
- In direct methods it seems to us that most of the work is in the factorization, less in the substitution steps.
 - Fully populated A: n^2 entries
 - ★ $1/3n^3 + O(n^2)$ complexity of Cholesky
 - * $2/3n^3 + O(n^2)$ complexity of LU (factorizable A)
 - ★ Substitution steps: only $O(n^2)$
- In iterative methods this can be less. Like $O(n^{5/2})$ in the fully-populated model cases (that fulfill an assumption on the convergence).
 - The issue of convergence of iterative methods is much more complicated, not discussed here

- The complexity issues are more involved: not only due to different matrix properties, but also due to hardware for computation and communication
 - Nowadays, nearly nothing is really sequential
 - \blacktriangleright CPU \rightarrow a mixture of powerful processors, coprocessors, cores, GPUs, and so on.
 - Furthermore, arithmetic operations are much faster than communication-based operations. And can be even accelerated by less accurate computation.

- The complexity issues are more involved: not only due to different matrix properties, but also due to hardware for computation and communication
 - Nowadays, nearly nothing is really sequential
 - ► CPU → a mixture of powerful processors, coprocessors, cores, GPUs, and so on.
 - Furthermore, arithmetic operations are much faster than communication-based operations. And can be even accelerated by less accurate computation.
- All of this helps to push the research on.

The question which class is better to solve our problems is ill-posed

Iterative methods complement approximate direct methods

- Direct methods may provide a less accurate solution due to possible relaxations.
- Making solution more accurate: use preprocessing or postprocessing by an auxiliary iterative method.

Iterative methods complement approximate direct methods

- Direct methods may provide a less accurate solution due to possible relaxations.
- Making solution more accurate: use preprocessing or postprocessing by an auxiliary iterative method.

Approximate direct methods complement iterative methods

- Pure iterative methods converge typically poorly. Or may have a low final attainable accuracy.
- Should be accompanied by a problem transformation based on a preconditioner. As:

MAx = Mb or AMy = b, y = Mx

• Preconditioner M may approximate A or A^{-1} .

Borderline between the use of direct and iterative methods is fuzzy

What else? The structure

- Matrix may contain a lot of zeros
- Nonzeros in *A* (left) and its factors (right) look like:





What else? The structure

- Matrix may contain a lot of zeros.
- Nonzeros in *A* (left).
- Nonzeros in its factors (right) can look like much better if A was preprocessed by a reordering: A → PAP^T, A → PAQ.



Basic Terminology: sparsity

Sparsity: so let us define it

• *A* is a sparse matrix if many of its entries are zero.



The sparsity pattern: $S{A} = {(i, j) | a_{ij} \neq 0, 1 \le i, j \le n}$.

Sparsity: more formally

• Attempts to formalize the sparsity more precisely like:

Definition

Matrix $A \in \mathbb{R}^{m \times n}$ is said to be sparse if it has $O(\min\{m, n\})$ nonzero entries. Another possibility: if A has row counts bounded by $r_{max} \ll n$ and/or column counts bounded by $c_{max} \ll m$.

Definition

Matrix $A \in \mathbb{R}^{m \times n}$ is said to be sparse if its number of nonzero entries is $O(n^{1+\gamma})$ for some $\gamma < 1$.

Sparsity: more formally

Attempts to formalize the sparsity more precisely like:

Definition

Matrix $A \in \mathbb{R}^{m \times n}$ is said to be sparse if it has $O(\min\{m, n\})$ nonzero entries. Another possibility: if A has row counts bounded by $r_{max} \ll n$ and/or column counts bounded by $c_{max} \ll m$.

Definition

Matrix $A \in \mathbb{R}^{m \times n}$ is said to be sparse if its number of nonzero entries is $O(n^{1+\gamma})$ for some $\gamma < 1$.

Definition

(pragmatic, application-based definition: J.H. Wilkinson) Matrix $A \in \mathbb{R}^{m \times n}$ is said to be sparse if the fact that a part of its entries is equal to zero can be (algorithmically) exploited.

Is the (sparsity) structure really so important?

- Our claim is: yes, it is.
- But, should be used jointly with other computational concepts.
- Let us mention two new and interesting concepts: to show that the importance of exploiting sparsity is not disappearing.

Is the (sparsity) structure really so important?

- Our claim is: yes, it is.
- But, should be used jointly with other computational concepts.
- Let us mention two new and interesting concepts: to show that the importance of exploiting sparsity is not disappearing.

Concept 1: low-rank approximation

- Blocks expressed as products of matrices of low-rank: $B \in \mathbb{R}^{k \times l} \rightarrow B = EF^T$ with $E \in \mathbb{R}^{k \times r}$ and $F \in \mathbb{R}^{l \times r}$
- The two factors of low rank a) may occupy less memory, b) may be cheaper in matrix-matrix products (matvecs).

- Often implied by appplications
- Like: panel clustering in BEM (Hackbusch, Nowak, 1989), the multipole method (Greengard, Rokhlin, 1997), mosaic-skeleton approximations (Tyrtyshnikov, 1996) etc.: an example of a hierarchical (data-sparse) matrix:

- Often implied by appplications
- Like: panel clustering in BEM (Hackbusch, Nowak, 1989), the multipole method (Greengard, Rokhlin, 1997), mosaic-skeleton approximations (Tyrtyshnikov, 1996) etc.: an example of a hierarchical (data-sparse) matrix:



- Often implied by appplications
- Like: panel clustering in BEM (Hackbusch, Nowak, 1989), the multipole method (Greengard, Rokhlin, 1997), mosaic-skeleton approximations (Tyrtyshnikov, 1996) etc.: an example of a hierarchical (data-sparse) matrix:



- Often implied by appplications
- Like: panel clustering in BEM (Hackbusch, Nowak, 1989), the multipole method (Greengard, Rokhlin, 1997), mosaic-skeleton approximations (Tyrtyshnikov, 1996) etc.: an example of a hierarchical (data-sparse) matrix:



- Often implied by appplications
- Like: panel clustering in BEM (Hackbusch, Nowak, 1989), the multipole method (Greengard, Rokhlin, 1997), mosaic-skeleton approximations (Tyrtyshnikov, 1996) etc.: an example of a hierarchical (data-sparse) matrix:



Accelerating by low-rank compression: sparsity still needed



Accelerating by low-rank compression: sparsity still needed



- More complex applications: algebraic variations needed.
- But then: generalized schemes need exploiting the classical (blockwise) sparsity outside a specific hierarchical scheme.

Concept 2: Accelerating by low precision computation

- Traditionally: single precision (fp32) and double precision (fp64)
- Throughout 1990's, fp32 was not much faster than fp64.
- Real breakthrough: (SSE units, Intel, 1999): fp32 significantly accelerated
- Emergence of half precision (fp16) floating-point arithmetic: 2008 revision of the IEEE standard.

Concept 2: Accelerating by low precision computation

- fp16 started as storage format, but soon in GPU accelerators. See discussions in Higham, 2017; Higham, Mary, 2022.
- BUT: fp16: limited range (largest positive number is 6.55×10^4); also bfloat16 (Google, tensor processing units, larger range)

Table: Parameters for bfloat16, fp16, fp32, and fp64 arithmetic: the number of bits, u, smallest positive (subnormal) number x_{min}^s , smallest normalized positive number x_{min} , and largest finite number x_{max} .

	Signif.	Exp.	u	x_{min}^s	x_{min}	x_{max}
fp16	11	5	4.88×10^{-4}	5.96×10^{-8}	6.10×10^{-5}	6.55×10^4
fp32	24	8	5.96×10^{-8}	1.40×10^{-45}	1.18×10^{-38}	3.40×10^{38}
fp64	53	11	1.11×10^{-16}	4.94×10^{-324}	2.22×10^{-308}	1.80×10^{308}
bfloat16	8	8	3.91×10^{-3}	not used	1.18×10^{-38}	3.39×10^{38}

Concept 2: Accelerating by using low precision: Boeing/msc01050

- Left: A in standard double precision (fp64)
- Right: *A* in the half precision (fp16)



• In fp16, we get only an approximation

Concept 2: Accelerating by using low precision: Boeing/msc01050

- Left: Cholesky factor L in standard double precision (fp64)
- Right: Cholesky factor *L* in the half precision (fp16)



fp16: similar fill (ratio 2.247 versus 2.534 for fp16)
low precision, but sparsity factorization problems are still here: they should be considered also here. Rough comparison of extreme cases of dense and sparse \boldsymbol{A}

	Dense	matrix	Sparse matrix			
dim	space	dec time (s)	dim	space	dec time (s)	
3000	4.5M	5.72	10000	40k	0.02	
4000	8M	14.1	90000	0.36M	0.5	
5000	12.5M	27.5	1M	4M	16.6	
6000	18M	47.8	2M	8M	49.8	

Rough comparison of extreme cases of dense and sparse \boldsymbol{A}

	Dense	matrix	Sparse matrix			
dim	space	dec time (s)	dim	space	dec time (s)	
3000	4.5M	5.72	10000	40k	0.02	
4000	8M	14.1	90000	0.36M	0.5	
5000	12.5M	27.5	1M	4M	16.6	
6000	18M	47.8	2M	8M	49.8	

- Recall the pragmatic definition.
- The decision whether to use or not to use depends also on what we know about the computation.
- In sparse direct methods we know a lot.
- Clearly, exploiting sparsity is a must. But, the question is how.

Basic Terminology: blocks

Blocks: why we like them

Blocks: why we like them

- Contemporary terminology related to computations emphasizes the most limiting factor.
- Algorithms are compute-bound, memory-bound or latency-bound.
- Most chips are designed such that dense matrix-matrix multiply, which typically performs k³ operations on k² data can run at full compute throughput

↓ BLOCKS

• We may use large (I.) or small (II.) blocks
I. Large blocks

- Connected to reducibility or input (application)
- $A \in \mathbf{R}^{n \times n}$ is reducible, if it can be permuted as

$$P^T A P = \begin{pmatrix} A_{11} & A_{12} \\ & A_{22} \end{pmatrix}$$

 A_{11} and A_{22} are square matrices of dimensions at least 1.

- If *A* is not reducible, it is called irreducible.
- A reducible: block factorization/substitution (permutation omitted).

$$\begin{pmatrix} A_{11} & A_{12} \\ & A_{22} \end{pmatrix} \begin{pmatrix} x_1 \\ x_2 \end{pmatrix} = \begin{pmatrix} b_1 \\ b_2 \end{pmatrix} \to x_2 = A_{22}^{-1} b_2, x_1 = A_{11}^{-1} (b_1 - A_{12} x_2)$$

I. Large blocks

- Connected to reducibility or input (application)
- $A \in \mathbf{R}^{n \times n}$ is reducible, if it can be permuted as

$$P^T A P = \begin{pmatrix} A_{11} & A_{12} \\ & A_{22} \end{pmatrix}$$

 A_{11} and A_{22} are square matrices of dimensions at least 1.

- If *A* is not reducible, it is called irreducible.
- A reducible: block factorization/substitution (permutation omitted).

$$\begin{pmatrix} A_{11} & A_{12} \\ & A_{22} \end{pmatrix} \begin{pmatrix} x_1 \\ x_2 \end{pmatrix} = \begin{pmatrix} b_1 \\ b_2 \end{pmatrix} \to x_2 = A_{22}^{-1} b_2, x_1 = A_{11}^{-1} (b_1 - A_{12} x_2)$$

• Large blocks can be also a result of a (saddle-point) input A, like:

$$A = \begin{pmatrix} B & E \\ F & C \end{pmatrix},$$

A lot of specialized approaches. Solving subproblems is not principially different from standard sparse approaches.

II. small blocks (only symmetric variant mentioned)

0

$$A = (A_{ib, jb}), \ A_{ib, jb} \in \mathbb{R}^{n_i \times n_j}, \ 1 \le ib, jb \le nb,$$

that is,

$$A = \begin{pmatrix} A_{1,1} & A_{1,2} & \cdots & A_{1,nb} \\ A_{2,1} & A_{2,2} & \cdots & A_{2,nb} \\ \vdots & \vdots & \ddots & \vdots \\ A_{nb,1} & A_{nb,2} & \cdots & A_{nb,nb} \end{pmatrix}$$

• Assuming nonsingular square blocks $A_{jb,jb}$ on the diagonal.

II. small blocks (only symmetric variant mentioned)

۲

$$A = (A_{ib,jb}), \ A_{ib,jb} \in \mathbb{R}^{n_i \times n_j}, \ 1 \le ib, jb \le nb,$$

that is,

$$A = \begin{pmatrix} A_{1,1} & A_{1,2} & \cdots & A_{1,nb} \\ A_{2,1} & A_{2,2} & \cdots & A_{2,nb} \\ \vdots & \vdots & \ddots & \vdots \\ A_{nb,1} & A_{nb,2} & \cdots & A_{nb,nb} \end{pmatrix}$$

- Assuming nonsingular square blocks $A_{jb, jb}$ on the diagonal. Implications
- Large blocks: as we have seen above: only an additional hierarchical level.

 Small blocks: Pointwise factorizations can be formulated blockwise:
entries → submatrices. Here not reminded, but expected.

Outline

Introduction

Factorizations

- 3 Symbolic Cholesky factorization
- Sparse matrices and data structures
- 5 (Numerical) Cholesky factorization
- Sparse LU factorization
- Stability, ill-conditioning, indefiniteness
- 8 Symmetric indefinite factorization
- Sparse Least Squares and factorizations
- 10 Reorderings
- Algebraic preconditioning

Introduction to factorizations

- Traditional way: Gaussian elimination: systematic columnwise annihilation of entries in the lower triangular part of *A*.
- Formally a sequential multiplications by column elimination matrices (*A* factorizable) getting the elimination sequence:

$$A^{(1)} \to A^{(2)} = C_1 A^{(1)} \to A^{(3)} = C_2 C_1 A^{(1)} \to \dots \to A^{(n)} = C_{n-1} \dots C_1 A^{(1)}$$

• Elementwise, $(a_{11} = a_{11}^{(1)} \neq 0)$, the first step $C_1 A^{(1)} = A^{(2)}$ is

$$\begin{pmatrix} 1 & & & \\ -a_{21}^{(1)}/a_{11}^{(1)} & 1 & \\ -a_{31}^{(1)}/a_{11}^{(1)} & 1 & \\ \vdots & & & 1 \\ -a_{n1}^{(1)}/a_{11}^{(1)} & & & 1 \end{pmatrix} \begin{pmatrix} a_{11}^{(1)} & a_{12}^{(1)} & \dots & a_{1n}^{(1)} \\ a_{21}^{(1)} & a_{22}^{(1)} & \dots & a_{2n}^{(1)} \\ a_{31}^{(1)} & a_{32}^{(1)} & \dots & a_{3n}^{(1)} \\ \vdots & \vdots & \ddots & \vdots \\ a_{n1}^{(1)} & a_{n2}^{(1)} & \dots & a_{nn}^{(1)} \end{pmatrix} = \begin{pmatrix} a_{11}^{(1)} & a_{12}^{(1)} & \dots & a_{1n}^{(1)} \\ 0 & a_{22}^{(2)} & \dots & a_{2n}^{(2)} \\ 0 & a_{32}^{(2)} & \dots & a_{3n}^{(2)} \\ \vdots & \vdots & \ddots & \vdots \\ 0 & a_{n2}^{(2)} & \dots & a_{nn}^{(2)} \end{pmatrix},$$

Introduction to factorizations

- The *k*-th partially eliminated matrix is $A^{(k)}$.
- The product of inverted column elimination matrices

$$\begin{pmatrix} 1 & & & \\ a_{21}^{(1)}/a_{11}^{(1)} & 1 & & \\ a_{31}^{(1)}/a_{11}^{(1)} & a_{32}^{(2)}/a_{22}^{(2)} & 1 & \\ \vdots & \vdots & \vdots & 1 & \\ a_{n1}^{(1)}/a_{11}^{(1)} & a_{n2}^{(2)}/a_{22}^{(2)} & \vdots & \vdots & 1 \end{pmatrix}$$

• That is, we have the LU factorization

$$A = A^{(1)} = C_1^{-1} C_2^{-1} \dots C_{n-1}^{-1} A^{(n)} = LU.$$

• There are more ways differing in relative order of elimination steps that are the same even in finite precision arithmetic!

LU written in matrix/vector form: submatrix LU

• The first step (k = 1):

$$C_{1}A = \begin{pmatrix} 1 \\ -v/a_{11} & I \end{pmatrix} \begin{pmatrix} a_{11} & u^{T} \\ v & A_{2:n,2:n} \end{pmatrix} = \begin{pmatrix} a_{11} & u^{T} \\ A_{2:n,2:n} - vu^{T}/a_{11} \end{pmatrix},$$

$$v = (a_{21}, \dots, a_{n1})^{T}, \quad (l_{21}, \dots, l_{n1})^{T} = v/a_{11}, \quad u^{T} = (a_{12}, \dots, a_{1n}).$$

The $(n-1) \times (n-1)$ active submatrix

$$A^{(2)} = S = A_{2:n,2:n} - vu^T / a_{11}$$

is the Schur complement of A with respect to a_{11} .

• A is factorizable \Rightarrow S is factorizable, and the process can be repeated.

Submatrix LU

- The elimination: sequence of rank-one updates applied to the Schur complements.
- After k 1 steps $(1 < k \le n)$:

$$S^{(k)} = \begin{pmatrix} a_{kk} & \dots & a_{kn} \\ \vdots & \ddots & \vdots \\ a_{nk} & \dots & a_{nn} \end{pmatrix} - \sum_{j=1}^{k-1} \begin{pmatrix} l_{kj} \\ \vdots \\ l_{nj} \end{pmatrix} \begin{pmatrix} u_{jk} & \dots & u_{jn} \end{pmatrix} = \begin{pmatrix} a_{kk}^{(k)} & \dots & a_{kn}^{(k)} \\ \vdots & \ddots & \vdots \\ a_{nk}^{(k)} & \dots & a_{nn}^{(k)} \end{pmatrix} = A_{k:n,k:n}^{(k)}.$$

Schematically:



Another relative order of operations: Column LU

• Consider first *j* columns of *A*: they must satisfy

$$\begin{pmatrix} A_{1:j-1,1:j-1} & A_{1:j-1,j} \\ A_{j:n,1:j-1} & A_{j:n,j} \end{pmatrix} \to \begin{pmatrix} L_{1:j-1,1:j-1} & \\ L_{j:n,1:j-1} & L_{j:n,j} \end{pmatrix} \begin{pmatrix} U_{1:j-1,1:j-1} & U_{1:j-1,j} \\ & u_{jj} \end{pmatrix}$$



Another relative order of operations: Column LU

• Consider first *j* columns of *A*: we must have

$$\begin{pmatrix} A_{1:j-1,1:j-1} & A_{1:j-1,j} \\ A_{j:n,1:j-1} & A_{j:n,j} \end{pmatrix} \to \begin{pmatrix} L_{1:j-1,1:j-1} & \\ L_{j:n,1:j-1} & L_{j:n,j} \end{pmatrix} \begin{pmatrix} U_{1:j-1,1:j-1} & U_{1:j-1,j} \\ & u_{jj} \end{pmatrix}$$

• This implies conditions for the two phases of computation (column of *U* and *L*):

$$U_{1:j-1,j} = L_{1:j-1,1:j-1}^{-1} A_{1:j-1,j}, \quad u_{jj} = a_{jj} - L_{j,1:j-1} U_{1:j-1,j},$$
$$l_{jj} = 1, \ L_{j+1:n,j} = (A_{j+1:n,j} - L_{j+1:n,1:j-1} U_{1:j-1,j})/u_{jj}.$$

• The factors can be computed column by column:

$$1 \rightarrow \ldots j \rightarrow \ldots n$$

- Easy embedding of the row permutation: $A \rightarrow PA$
- Scheme by rows: computing columns of A^T

Schemes described as a generic scheme of three nested loops

Algorithm (Generic LU factorization)



• The crucial pointwise operation:

$$a_{ij} = a_{ij} - a_{ik}a_{kk}^{-1}a_{kj} \equiv a_{ij} = a_{ij} - l_{ik}a_{kj}$$

Schemes differ by treating sparsity, vectorization etc.

Cholesky factorization: also three basic ways

- Left-looking schemes (second phase of the column LU)
- Right-looking schemes (submatrix scheme that computes only quantities in *L*)





 But there is also the row scheme based on the first phase (solve) of the column LU

Column (left-looking) Cholesky

Algorithm

Column Cholesky factorization: $A \rightarrow$ square-root factor $L = (l_{ij})$ 1. for j = 1 : n do 2. Compute an auxiliary vector $t_{j:n}$

$$\begin{pmatrix} t_j \\ \vdots \\ t_n \end{pmatrix} = \begin{pmatrix} a_{jj} \\ \vdots \\ a_{nj} \end{pmatrix} - \sum_{\{k \mid l_{jk} \neq 0\}} l_{jk} \begin{pmatrix} l_{jk} \\ \vdots \\ l_{nk} \end{pmatrix}$$

3. Get a column of L by scaling $t_{j:n}$

$$\begin{pmatrix} l_{jj} \\ \vdots \\ l_{nj} \end{pmatrix} = \frac{1}{\sqrt{t_j}} \begin{pmatrix} t_j \\ \vdots \\ t_n \end{pmatrix}$$

4. end *j*

(2)

(1)

Cholesky factorization: row scheme

- But there is also the row scheme.
- The row scheme is based on the first phase (solve) of the column LU



• Easy to implement column permutation $A \rightarrow AP$.

Factorizations and sparsity

 Factorizations of sparse matrices may create new nonzero entries outside S{A} called fill/fill-in/filled entries

• Fill-in means more operations, more memory

Factorizations and sparsity

• Can we expect that some nonzeros become zeros due to cancellation?

Factorizations and sparsity

- Can we expect that some nonzeros become zeros due to cancellation?
- Very rarely.
- We assume non-cancellation: the result of adding, subtracting or multiplying two nonzeros is nonzero again.
- This implies:

$$\mathcal{S}\{A\} \subseteq \mathcal{S}\{L+U\}.$$

- Non-cancellation implies a possibility to deal with the structure only using graphs to determine the fill-in (if factorizability guaranteed [©])
- Let us go to see the fill-in results

Simple fill-in results: one step of factorization



• Summarized as the fill-in lemma: one step of the fill-in

Lemma

Let $i, j, k \in \{1, \dots, n\}$, step $k < \min\{i, j\} \le n$. Then

$$a_{ij}^{(k)} \neq 0 \Longleftrightarrow a_{ij}^{(k-1)} \neq 0 \, \lor \, (a_{ik}^{(k-1)} \neq 0 \, \land \, a_{kj}^{(k-1)} \neq 0)$$

Fill-in during the factorization: more steps

• But we have the sequence (of Schur complements)

$$S^{(1)} \to S^{(2)} \to S^{(3)} \to \dots \to S^{(n)} = a_{nn}^{(n-1)}.$$

• With sparsity structures $\mathcal{S}(S^{(i)})$ representing the elimination graphs

$$\mathcal{G}^1 \equiv \mathcal{G}(A), \mathcal{G}^2, \dots, \mathcal{G}^n, \mathcal{G}^k = (\mathcal{V}^k, \mathcal{E}^k)$$

Fill-in during the factorization: more steps

- But we have the sequence (of Schur complements) $S^{(1)} \rightarrow S^{(2)} \rightarrow S^{(3)} \rightarrow \ldots \rightarrow S^{(n)} = a_{nn}^{(n-1)}.$
- With sparsity structures $\mathcal{S}(S^{(i)})$ representing the elimination graphs

$$\mathcal{G}^1 \equiv \mathcal{G}(A), \mathcal{G}^2, \dots, \mathcal{G}^n, \mathcal{G}^k = (\mathcal{V}^k, \mathcal{E}^k)$$

• The fill-in in the sequence is described by the Parter's rule:

To obtain the elimination graph \mathcal{G}^{k+1} from \mathcal{G}^k , delete vertex k and add all edges $(i \xrightarrow{\mathcal{G}^{k+1}} j)$ such that $(i \xrightarrow{\mathcal{G}^k} k)$ and $(k \xrightarrow{\mathcal{G}^k} j)$.

 $\mathcal{V}^{k+1} = \mathcal{V}^k \setminus \{k\}, \ \mathcal{E}^{k+1} = \mathcal{E}^k \cup \{(i,j) \mid i,j \in adj_{\mathcal{G}^k}\{k\}\} \setminus \{(i,k) \mid i \in adj_{\mathcal{G}^k}\{k\}\}.$

 The reason that graphs can be used: the non-cancellation assumption: once created fill-in remains ©

• A (nonsymmetric) example



Figure: The original digraph $\mathcal{G} = \mathcal{G}^1$ (left) and the directed elimination graph \mathcal{G}^2 (right). The red dashed lines denote fill edges.

• S(A) symmetric: the adjacency set of vertex *k* forms a clique.



Figure: The original undirected graph $\mathcal{G} = \mathcal{G}^1$ (left) and the obtained graph \mathcal{G}^2 (right). The red dashed lines denote fill edges. The vertices {2,3,4} become a clique.

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• But the Parter's rule is only a local rule. The following theorem fully characterizes the fill-in in the factors.

Theorem

Let A = LU. Then $S(L + U)_{ij} \neq 0$ if and only if there is a fill-path $i \xrightarrow{\mathcal{G}(A)} j$. The fill-paths may not be unique.



 $p_2 < p_3 < p_1 < p_4 < \min(i, j)$

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• But the Parter's rule is only a local rule. The following theorem fully characterizes the fill-in in the factors.

Theorem

Let A = LU. Then $S(L + U)_{ij} \neq 0$ if and only if there is a fill-path $i \xrightarrow{\mathcal{G}(A)} j$. The fill-paths may not be unique.



 $p_2 < p_3 < p_1 < p_4 < \min(i, j)$

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• But the Parter's rule is only a local rule. The following theorem fully characterizes the fill-in in the factors.

Theorem

Let A = LU. Then $S(L + U)_{ij} \neq 0$ if and only if there is a fill-path $i \xrightarrow{\mathcal{G}(A)} j$. The fill-paths may not be unique.



 $p_2 < p_3 < p_1 < p_4 < \min(i, j)$

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• But the Parter's rule is only a local rule. The following theorem fully characterizes the fill-in in the factors.

Theorem

Let A = LU. Then $S(L + U)_{ij} \neq 0$ if and only if there is a fill-path $i \xrightarrow{\mathcal{G}(A)} j$. The fill-paths may not be unique.



 $p_2 < p_3 < p_1 < p_4 < \min(i, j)$

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• But the Parter's rule is only a local rule. The following theorem fully characterizes the fill-in in the factors.

Theorem

Let A = LU. Then $S(L + U)_{ij} \neq 0$ if and only if there is a fill-path $i \xrightarrow{\mathcal{G}(A)} j$. The fill-paths may not be unique.

• Demonstrate this: starting with a path $(i, p_1, p_2, p_3, p_4, j)$ in $\mathcal{G}(A)$



 $p_2 < p_3 < p_1 < p_4 < \min(i, j)$

From the Parter's rule for factors to fill paths in $\mathcal{G}(A)$

• Symmetric $S{A}$: a filled entry in position (8, 6) of L because of the fill-path $8 \xleftarrow{\mathcal{G}(A)}{min} 6: 8 \longleftrightarrow 2 \longleftrightarrow 5 \longleftrightarrow 1 \longleftrightarrow 6.$



• Too complicated to be exploited algorithmically

- Too complicated to be exploited algorithmically
- Something that is even simpler than the fill paths needed.

- Too complicated to be exploited algorithmically
- Something that is even simpler than the fill paths needed.
- The symmetric case: SPD matrix is always factorizable → using graphs to model A = LL^T.
- The dependence: replication of entries among columns.

Outline



Symbolic Cholesky

Column replication: as a sequence



Symbolic Cholesky

Column replication: as a sequence



	1	2	3	4	5	6	7	8
1	(*	*				*	*	* \
2	*	*	*		f	f	*	*
3		*	*					
4				*				
5	*	f			*			
6	*	f				*		
7	*	*					*	
8	\ *	*						*/

Nonzero entries of the lower triangular part
Column replication: as a sequence



	1	2	3	4	5	6	7	8
1	(*	*				*	*	* \
2	*	*	*		f	f	*	*
3		*	*		f	f	f	f
4				*				
5	*	f	f		*			
6	*	f	f			*		
7	*	*	f				*	
8	/ *	*	f					*/

Nonzero entries of the lower triangular part

Column replication formally

• First observation:

For any $i > j \ge 1$ such that $l_{ij} \ne 0$

$$\mathcal{S}\{L_{i:n,j}\}\subseteq \mathcal{S}\{L_{i:n,i}\}.$$

This is called the column replication principle.



(3)

Column replication: as a sequence



• First row entries of L^T are sufficient to guarantee the replication.

Column replication: as a sequence



- First row entries of L^T are sufficient to guarantee the replication.
- They represent a directed acyclic graph (DAG) $\mathcal{T}(A) \subseteq \mathcal{G}(L^T)$.
- $\mathcal{T}(A)$: a special case of the transitive reduction of $\mathcal{G}(L^T)$ (simplest DAG that preserves paths in $\mathcal{G}(L^T)$.

Column replication: as a sequence



- First row entries of L^T are sufficient to guarantee the replication.
- They represent a directed acyclic graph (DAG) $\mathcal{T}(A) \subseteq \mathcal{G}(L^T)$.
- $\mathcal{T}(A)$: a special case of the transitive reduction of $\mathcal{G}(L^T)$ (simplest DAG that preserves paths in $\mathcal{G}(L^T)$.
- Equivalently: edges of T(A) ↔ first subdiagonal entries of L, denoted parent(j): parent(j) = min{i | i > j, l_{ij} ≠ 0}.

Replication of column structures once more



That DAG is a tree or forest, called the elimination tree



• Subtree $\mathcal{T}(5)$ includes vertices 1, 2, 3, 4, 5; $|\mathcal{T}(5)| = 5$;

That DAG is a tree or forest, called the elimination tree



No need to use arrows.

Side-effect of column replication: row replication



• $a_{8,3} \neq 0 \Rightarrow l_{8,4} \neq 0 \Rightarrow l_{8,5} \neq 0$ and so on

• This is equivalent to passing row fill up the tree due to $a_{8,3} \neq 0$.

Side-effect of column replication: row replication: shown again



• Replication of columns \Rightarrow replication in a particular row.

Side-effect of column replication: row replication: shown again



- Replication of columns \Rightarrow replication in a particular row.
- When such row replication starts? If the first entry belongs to A!

Necessary and sufficient condition for a fill-in entry

• No $k \ge 1$ with $a_{ik} \ne 0$, no replication of nonzeros in row *i* can start.

Necessary and sufficient condition for a fill-in entry

No k ≥ 1 with a_{ik} ≠ 0, no replication of nonzeros in row i can start.
Otherwise, there is a nonzero in A_{i*} that starts the row replication.



Necessary and sufficient condition for a fill-in entry

No k ≥ 1 with a_{ik} ≠ 0, no replication of nonzeros in row i can start.
Otherwise, there is a nonzero in A_{i*} that starts the row replication.



Theorem

Let *A* be SPD and let *L* be its Cholesky factor. If $a_{ij} = 0$ for some $1 \le j < i \le n$ then there is a filled entry $l_{ij} \ne 0$ if and only if there exists k < j and $t \ge 1$ such that $a_{ik} \ne 0$ and $parent^t(k) = j$.

63/174

Taking all replications in row i, we have its structure in L.



• The subgraph of $\mathcal{T}(A)$ determines it

- Detached by k, k', k'' and k''' from below (corresponding to nonzeros a_{i,k}, a_{i,k'}, a_{i,k''} and a_{i,k'''}), by i from above.
- called the *i*-th row subtree of $\mathcal{T}(A)$.

Its vertices precisely determine nonzeros in the *i*-th row of L.

• But, for factorization we may prefer to know column structure of L

It would be nice to know column sparsity patterns of L as well

• Repetition: Row structures: going up $\mathcal{T}(A)$ from nonzeros of A (k, k', k'' and k''').



It would be nice to know column sparsity patterns of L as well

• Repetition: Row structures: going up $\mathcal{T}(A)$ from nonzeros of A (k, k', k'' and k''').



- Column structures: merging column lists: $col_L\{j\} = adj_{\mathcal{G}(A)}\{\mathcal{T}(j)\}$ e.g., $col_L\{5\} = adj_{\mathcal{G}(A)}(1) \cup adj_{\mathcal{G}(A)}(2) \cup adj_{\mathcal{G}(A)}(3) \cup adj_{\mathcal{G}(A)}(5)$
- Up the tree. This is clear, but implementation may be funny.

Getting column structures more efficiently

First define: A labeling of the vertices of a tree (and, more generally, in a DAG) is a topological ordering if, for all *i* and *j*, *j* ∈ desc_T{*i*} implies *j* < *i*



Getting column structures more efficiently

First define: A labeling of the vertices of a tree (and, more generally, in a DAG) is a topological ordering if, for all *i* and *j*, *j* ∈ desc_T{*i*} implies *j* < *i*



- Apparently, the second labeling is better.
- Why? It localizes. \rightarrow
- S(L) by columns is obtained by merging columns, the merged columns should not wait too long to be merged again, in order to use small intermediate memory.

All topological orderings are nice

Sparsity patterns of the Cholesky factors of A and PAP^T can be different, but the amount of fill-in is the same.

Theorem

Let $S{A}$ be symmetric. If *P* is the permutation matrix corresponding to a topological reordering of the elimination tree T of *A* then the filled graphs of *A* and PAP^T are isomorphic.

- Topological orderings do not change fill-in size
- In the other words, the amount of fill-in is invariant under the class of topological reorderings of the elimination tree.

... are nice. But, some topological orderings are nicer: postordering

 A topological ordering of T is a postordering if the vertex set of any subtree T(i) (i = 1,...,n) is a contiguous sublist of 1,...,n.



- Postordering is even more localizing labeling.
- Needed in fast algorithms.

Ooops. We still do not have the elimination tree. How to get it?

To find *T*(*A*), we just mimick the row replication: scan *A* by rows for *i* = 1,..., *n* − 1 and go up the constructed part of *T*(*A*) to attach *i* as a temporary root.



 During the search if the *i*-th row, vertex *i* is either put on the top of the current structure or added as an isolated vertex if not connected to the rest of T(A) yet.

Constructing elimination tree: complexity

A complexity problem

- $\mathcal{T}(A)$: parent(6) = 0; parent(i) = i + 1, i = 1, ..., 5.
- For each *i* we start from a_{i1} and attach *i* at the top of the partial \mathcal{T} : $O(n^2)$ complexity
- But, improvements lead to the nearly linear complexity of getting $\mathcal{T}(A)$.

What else: blocks

• They look like this. In L! They are called the supernodes.



Replication principle increases their probability.

Supernodes and efficient computation

- the loop over columns of the updating supernode can be unrolled to save memory references (dense BLAS2)
- parts of the updating supernode can be used for blocks of updated supernode (dense BLAS3)



Supernodes: block-based elimination

• Supernodes imply the supernodal elimination (assembly) tree.



Their important type can be found in a nearly linear complexity.

Independence of subtrees: parallelism at hand

Theorem

Consider the elimination tree \mathcal{T} and the Cholesky factor L of A. Let $\mathcal{T}(i)$ and $\mathcal{T}(j)$ be two vertex-disjoint subtrees of \mathcal{T} . Then for all $s \in \mathcal{T}(i)$ and $t \in \mathcal{T}(j)$ the entry l_{st} of L is zero.



- Of course, $l_{st} = 0$. Otherwise t would have to be ancestor of s or vice versa.
- Column structures and columns can be merged independently. Contradiction with row replication.

Outline



Sparse vectors and matrices in a computer

Sparse data (matrix/row/column) in a computer: I. dynamic formats

a) Coordinate (or triplet format for data: individual entries of A held as triplets (*i*, *j*, *a_{ij}*), where *i* is the row index and *j* is the column index of the entry *a_{ij}* ≠ 0; similar for vectors

b) Linked list - based format: stores data as linked items



 Linked lists can be cyclic, one-way, two-way, etc., can be embedded into a larger array: emulated dynamic behavior

Sparse vectors and matrices in a computer

Sparse matrix storage: II. static formats

• CSR (Compressed Sparse Row) static format. The column indices compressed in the array colindA by rows. Sorted or unsorted. CSC: variant by columns.

	1	2	3	4	5											
1	(3.			-2.		Indices	1	2	3	4	5	6	7	8	9	10
2 3	_1	1.	3		4.	rowptrA	1	3	5	8	9	11				
4	1.		0.	1.	1.	colindA	1	4	2	5	1	3	5	4	2	5
5		7.			6. /	valA	3.	-2.	1.	4.	-1.	3.	1.	1.	7.	6.

- If *A* is symmetric, only the lower (or upper) triangular part stored.
- Possible to store only $S{A}$ and not numerical values.
- Useful: static, theory helps to use them efficiently

Sparse matrix storage: static versus dynamic formats

- dynamic data structures:
 - more flexible but this flexibility might not be needed
 - difficult to vectorize
 - difficult to keep spatial locality of rows and columns
 - used preferably for storing vectors
- static data structures:
 - ad-hoc insertions/deletions should be avoided (better algorithms)
 - much simpler to vectorize / utilize cache
 - efficient access to rows/columns

Sparse vectors and matrices in a computer

Simulating dynamic storage formats by static ones

- Dynamic storage formats can be simulated by
 - adding to CSR/CSC an elbow space for fill-in entries

- A mechanism to compress/extend such structure needed.
- Useful for approximate factorization with limited fill-in.

Outline

(Numerical) Cholesky factorization

Sparse Cholesky factorization: conceptual comments

- Efficient symbolic phase based on \mathcal{T} : explained:
 - ▶ Row/column counts of L known → storage can be allocated
 - Postordering enables a lot of other efficient algorithms
 - Blocks: supernodes
 - Technical tricks as splitting large supernodes into smaller panels to embed them into computer caches
- Numerical factorization: new important feature → more communication: this is described by a communication graph: DAG (directed acyclic)

Cholesky Factorization

Numerical Cholesky factorization: from operations to tasks







a) b)

C)

- cdiv(k): scaling column k by the square root of the diagonal entry
- cmod(j,k): column j modified by a multiple of column k

Algorithm

Sparse column (left-looking) Cholesky

6: end for

▷ All of them !!!!!!

Cholesky Factorization

Numerical Cholesky factorization: from operations to tasks







a)

b)



- cdiv(k): scaling column k by the square root of the diagonal entry
- cmod(j,k): column j modified by a multiple of column k

Algorithm

Sparse submatrix (right-looking) Cholesky

- 1: for $k=1:n\ {\it do}$
- 2: cdiv(k)
- 3: for $j \in Struct(L_{*k})$ do
- 4: cmod(j,k)
- 5: end for
- 6: end for
Splitting Cholesky factorization into tasks





C)

- cdiv(k): scaling column k by the square root of the diagonal entry
- cmod(j,k): column j modified by a multiple of column k



Large-grain column (left-looking) communication model



Large-grain submatrix (right-looking) communication model



Using supernodes: enhancing parallel processing

• Arithmetic of dense trapezoidal matrices. Sophisticated mappings among them.



- Dependencies captured by the communication (dependency) DAG.
- The tree parallelism.
- Block arithmetic.

Variations of the Cholesky factorization: sparsity and supernodes

- Left-looking approach:
 - Dependency DAG
 - Block arithmetic.
- Right-looking approach:
 - Dependency DAG
 - A specific popular approach: uses the supernodal elimination tree for dependencies: the multifrontal method
 - High level of memory efficiency due to computational locality: contributions to the Schur complement kept aside in a stack







Direct methods: Multifrontal method









Multifrontal method: another example matrix



Multifrontal method: details



Sparse Cholesky factorizations: up-looking factorization



- An alternative for sparse matrices is to compute *L* one row at a time. This is sometimes called an up-looking factorization.
- Asymptotically optimal, but difficult to incorporate high level BLAS.
- Also an efficient symbolic phase possible.
- High potential for approximate factorizations

Outline

Sparse LU factorization

Algebraic preconditioning

LU factorization and graphs and methods

• Differences with respect to Cholesky (roughly):

- ► Two factors: more general graph models (directed, bipartite) needed to describe *A* and the factors
- Problems with factorizability: symbolic and numerical steps cannot be always separated
- Due to this, sometimes stronger assumptions needed, sometimes on-the-fly changes: pivoting

Sparse LU factorization of generally nonsymmetric matrices

LU factorization: first symbolic model: DAGs



• Directed acyclic graphs for the factors capture their structure. We use $G(L^T)$ (*L* by columns, left) and G(U) (*U* by rows, right).



LU factorization and DAGs: alternating replications



• Alternating column and row replication (in the submatrix model).

• Left: *A*. Centre: showing one column replication. Right: also a row replication.

Recursive alternating replications

- Symmetric factorization: the recursive replications driven by the parents, subgraph of $G(L^T)$.
- In LU it is more interesting ©:
 - for columns of L: directed paths in U are used
 - for rows of U: directed paths in $\mathcal{G}(L^T)$.

Column replication in LU: example



Column replication in LU: example





Column replication in LU: example





Sparse LU: replications: funny game to detect paths

The path 1 → 3 → 5 → 6 in G(U). It implies the fill-in in L, first in column 3, then in columns 5 and 6.

• $2 \rightarrow 4 \rightarrow 5 \rightarrow 6$ in $\mathcal{G}(L^T) \Rightarrow$ fill-in at (4,7), (5,7) and (6,7) in U.



Something other than bothering with paths needed

- To employ $\mathcal{G}(L^T)$ and $\mathcal{G}(U)$ in efficient algorithms, they need to be simplified.
- They must be sparser and preserve reachability (transitive reduction adds also the edge set minimality condition).
- Remind: the elimination tree \mathcal{T} is a transitive reduction of $G(L^T)$.
- In LU, the analogy are transitive reductions $\mathcal{G}(L^T)$ and $\mathcal{G}(U)$.

Sparse LU factorization of generally nonsymmetric matrices

Transitive reductions of $\mathcal{G}(L^T)$ and $\mathcal{G}(U)$



Transitive reduction may be expensive to obtain

 Obtaining exact transitive reductions of G(L^T) and G(U) can be expensive on-the-fly due to the mutual dependency of the DAGs.

Transitive reduction may be expensive to obtain

- Obtaining exact transitive reductions of G(L^T) and G(U) can be expensive on-the-fly due to the mutual dependency of the DAGs.
- Instead, approximate reductions without the minimality condition may be computed. additional nonzeros do not make harm.
- We will call them equireachable DAGs (not fully transitively reduced DAGs).

Equireachability: example

Figure depicts $\mathcal{G}(U)$ and $\mathcal{G}'(U)$ for the matrix in Figure above.



Figure: The DAG $\mathcal{G}(U)$ (left) and $\mathcal{G}'(U)$ which is equireachable with $\mathcal{G}(U)$ (right).

• The edge (3, 6) is not in the transitive reduction.

Column sparsity patterns (for L)

• Schur complement description

$$\mathcal{S}\{L_{j:n,j}\} = \mathcal{S}\{A_{j:n,j}\} \bigcup_{k < j, u_{k,j} \neq 0} \mathcal{S}\{L_{j:n,k}\}, \quad 1 \le j \le n.$$

As in the symmetric case where the patterns are merged up T(A), not all the terms in this union are needed to get S{L_{j:n,j}}.
 Theorem shows this merging formally:

Theorem

If $\mathcal{G}'(U)$ is equireachable with $\mathcal{G}(U)$ then

$$\mathcal{S}\{L_{j:n,j}\} = \mathcal{S}\{A_{j:n,j}\} \bigcup_{(k \to j) \in \mathcal{E}(\mathcal{G}'(U))} \mathcal{S}\{L_{j:n,k}\}, \quad 1 \le j \le n.$$

Those in an equireachable graph are sufficient.
Similarly for sparsity patterns of the rows of U.

Getting an equireachable DAG: pruning of the elimination DAGs

Theorem

If for some j < s both $l_{sj} \neq 0$ and $u_{js} \neq 0$, then there are no edges $(j \rightarrow k)$ with k > s in the transitive reductions of $\mathcal{G}(U)$ and $\mathcal{G}(L^T)$.



• Pruning in $\mathcal{G}(L^T)$: green and blue nodes represent edges.

• $l_{kj} \neq 0$ and $u_{js} \neq 0$ imply $l_{ks} \neq 0$: $k \rightarrow s \Rightarrow$ The green ones can be removed.

Another graph model: column elimination tree

• An attractive idea for constructing $S\{L+U\}$ is based on using the column elimination tree $T(A^TA)$.

Theorem

Assume all the diagonal entries of *A* are nonzero and let $\hat{L}\hat{L}^T$ be the Cholesky factorization of A^TA . Then for any row permutation matrix *P* such that PA = LU

$$\mathcal{S}\{L+U\} \subseteq \mathcal{S}\{\widehat{L}+\widehat{L}^T\}.$$

• Very strong result (theoretically).

The column elimination tree

- A potential problem with the column elimination tree is that:
- $S{A^TA}$ can have significantly more nonzero entries than $S{L+U}$.

The column elimination tree

- A potential problem with the column elimination tree is that:
- $S{A^TA}$ can have significantly more nonzero entries than $S{L+U}$.
- An extreme example is when *A* has one or more dense rows because *A*^T*A* is then fully dense.
- So, using it or not using it, it depends.

Column elimination tree: example

• Standard elimination tree $\mathcal{T}(A)$.



• The elimination tree $\mathcal{T}(A^T A)$: much more dependencies, much less parallelism.



Other related issues: similar to Cholesky

- We can define supernodes (supernodal structure in *L* and *U*). Some compatibility between the factors is needed. Not mentioning the danger of pivoting.
- We can use a modified multifrontal method
- Typically distinguishing *A* with a nearly symmetric pattern from other situations.
- Note that the factorizability is not generally guaranteed.
Preprocessing for LU

• There exist preprocessing techniques that may alleviate problem of expensive LU.

I. Permuting nonzeros to the diagonal of \boldsymbol{A}

- This can be achieved by a nonsymmetric permutation like $A \rightarrow AQ$
- Terminology: The set of the diagonal entries of *A* is called the transversal.
- If *A* is nonsingular (even structurally only) then it can be nonsymmetrically permuted to have the full transversal.

Preprocessing for LU: I. getting full transversal



Preprocessing for LU: I. getting full transversal



Preprocessing for LU: I. getting full transversal



- Just a column (or row) permutation is needed.
- An algorithm to be used: bipartite graph matching
- It can consider also sizes of nonzero values: still cheap.

Preprocessing for LU: II. block triangular form

- When we can do this? If A is reducible.
- Remind that *A* is said to be reducible if there is a permutation matrix *P* such that

$$PAP^{T} = \begin{pmatrix} A_{p_{1},p_{1}} & A_{p_{1},p_{2}} \\ 0 & A_{p_{2},p_{2}} \end{pmatrix},$$

where A_{p_1,p_1} and A_{p_2,p_2} are non trivial square matrices (that is, they are of order at least 1).

Preprocessing for LU: II. block triangular form

- When we can do this? If A is reducible.
- Remind that *A* is said to be reducible if there is a permutation matrix *P* such that

$$PAP^{T} = \begin{pmatrix} A_{p_{1},p_{1}} & A_{p_{1},p_{2}} \\ 0 & A_{p_{2},p_{2}} \end{pmatrix},$$

where A_{p_1,p_1} and A_{p_2,p_2} are non trivial square matrices (that is, they are of order at least 1).

 Why do we do this? To be more happy ☺: factorize only diagonal blocks ⇒ do solves only with blocks.

Sparse LU: preprocessing to get BTF shape

Permutation to BTF: getting strong components An example of five SCCs: $\{p, q, r\}, \{s, t, u\}, \{v\}, \{w\}, \{x\}$.



- Shrinking the strong components: DAG. And the DAG can be always ordered to provide a block upper triangular matrix (blocks correspond to the strong components)
- The transformation is a vertex relabelling. This is a symmetric permutation $A \rightarrow PAP^{T}$.

Outline

Introduction
 Factorizations
 Symbolic Chol
 Sparse matrice
 (Numerical) Cl
 Sparse LU factorial

- Stability, ill-conditioning, indefiniteness
- Symmetric indefinite factorization
- Sparse Least Squares and factorizations
- Reorderings
- Algebraic preconditioning

Stability and ill-conditioning

Backward stability and ill-conditioning: standard points

• Consider getting factors as (L, U) = g(A). Two different notions.

Stability and ill-conditioning

Backward stability and ill-conditioning: standard points

- Consider getting factors as (L, U) = g(A). Two different notions.
- Backward stable algorithm: the computed factors (\hat{L}, \hat{U}) are the exact solution of $(\hat{L}, \hat{U}) = g(A + \Delta A)$ and ΔA (the backward error) is "small" for all possible inputs A.

Backward stability and ill-conditioning: standard points

- Consider getting factors as (L, U) = g(A). Two different notions.
- Backward stable algorithm: the computed factors (\hat{L}, \hat{U}) are the exact solution of $(\hat{L}, \hat{U}) = g(A + \Delta A)$ and ΔA (the backward error) is "small" for all possible inputs A.
- The problem (L, U) = g(A) is ill-conditioned if small perturbations in A can lead to large changes in (\hat{L}, \hat{U}) . The condition number then measures sensitivity of the output to the function input.

Backward stability and ill-conditioning: standard points

- Consider getting factors as (L, U) = g(A). Two different notions.
- Backward stable algorithm: the computed factors (\hat{L}, \hat{U}) are the exact solution of $(\hat{L}, \hat{U}) = g(A + \Delta A)$ and ΔA (the backward error) is "small" for all possible inputs A.
- The problem (L, U) = g(A) is ill-conditioned if small perturbations in A can lead to large changes in (\hat{L}, \hat{U}) . The condition number then measures sensitivity of the output to the function input.

Observation

Backward stability is a property of the computational algorithm. To compute solutions with a small backward error we need to consider stable algorithms. Ill-conditioning is a property of input problem data. To suppress the ill-conditioning, we need to transform the problem (a priori or a posteriori)

Sidestep: using the inverse instead of factorization

- No stability results (in contrast to factorization and solve): The computed inverse is typically not the exact inverse of a nearby matrix $A + \Delta A$ for any small perturbation ΔA .
- Impractical to compute and store A⁻¹, regardless of how sparse A is: see below: the matrix sparsity strikes back.

Sidestep: using the inverse instead of factorization

- No stability results (in contrast to factorization and solve): The computed inverse is typically not the exact inverse of a nearby matrix $A + \Delta A$ for any small perturbation ΔA .
- Impractical to compute and store A⁻¹, regardless of how sparse A is: see below: the matrix sparsity strikes back.

Theorem

A irreducible \Rightarrow the sparsity pattern $S\{A^{-1}\}$ is fully dense.

• This is the reason why inverses of *A* are not much used.

Improving the backward stability (and forcing factorizability)

- At step k of LU, the computed $a_{kk}^{(k)}$ (pivot) $(1 \le k < n)$ should be nonzero (to keep factorizability) and not of a small magnitude (to keep the growth in factors small).
- The growth can be measured by the growth factor:

$$\rho_{growth} = \max_{i,j,k} \left(\left| a_{ij}^{(k)} \right| / \left| a_{ij} \right| \right).$$
(4)

• Simple row interchanges: $A \rightarrow PA$ called partial pivoting ensures

$$|l_{ik}| \le 1 \Longrightarrow \max_{i>k} |a_{ik}^{(k)}| \le |a_{kk}^{(k)}|.$$

• Partial pivoting may not be sufficient. Complete pivoting is better, but it has much smaller potential for parallelization.

Stability and ill-conditioning

Pivoting possibilities

Partial pivoting:

$$o_{growth} \le 2^{n-1}.$$

• Complete pivoting choosing the pivot as an entry of the largest magnitude in the Schur complement.

$$\rho_{growth} \le n^{1/2} (2.3^{1/2}.4^{1/3} \dots n^{1/(n-1)})^{1/2}.$$

• Rook pivoting: the largest magnitude in its row and its column:

$$\rho_{growth} \le 1.5 \, n^{(3/4)\log n} \, .$$

Taking sparsity into account: threshold partial pivoting

$$\max_{i>k} |a_{ik}^{(k)}| \le \gamma^{-1} |a_{kk}^{(k)}|,$$

where $\gamma \in (0, 1]$ is a chosen threshold parameter.

 Even complete pivoting can be mixed with sparsity considerations: Markowitz pivoting

Outline

Symmetric indefinite factorization

- Reorderings
- Algebraic preconditioning

Stability and ill-conditioning

Symmetric indefinite matrix: example

Consider

$$A = \begin{pmatrix} \delta & 1 \\ 1 & 0 \end{pmatrix}.$$

• $\delta = 0 \Rightarrow$ LDLT with *D* diagonal does not exist.

• $\delta \ll 1 \Rightarrow$ LDLT with *D* diagonal is not stable since $\rho_{growth} = 1/\delta$.

• LDLT factorization generalized to allow D with 1×1 and $2 \times 2 \Rightarrow$ blocks. It preserves symmetry and is nearly as stable as the LU factorization.

$$A = \begin{pmatrix} 1 & 1 & 0 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{pmatrix} = \begin{pmatrix} 1 & 0 & 0 \\ 1 & 1 & 0 \\ 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \end{pmatrix} \begin{pmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \\ 0 & 0 & 1 \end{pmatrix} = LDL^{T}.$$

Here D has one 1×1 block and one 2×2 block.

Stability and ill-conditioning

Symmetric indefinite: balancing 1×1 pivots and 2×2 pivots

- Small growth for 1×1 pivot if $|a_{kk}|$ (a diagonal entry) is large.
- If such pivot not found, consider large off-diagonals
- Consider the inverse of the 2×2 block

$$\begin{pmatrix} a & b \\ b & d \end{pmatrix}^{-1} = \frac{1}{ad - b^2} \begin{pmatrix} d & -b \\ -b & a \end{pmatrix}$$

- \Rightarrow if |a|, |d| small with respect to $|b|, 2 \times 2$ pivot may be used.
- The standard rule balancing the pivots: based on requiring the same potential maximal growth in a 2 × 2 pivot versus two consecutive 1 × 1 pivots.
- This implies an appropriate parameter $(1 + \sqrt{17})/8$ to choose between the pivots (see the next slide)

$$\rho_{growth} < 3n\sqrt{2 \ 3^{1/2} 4^{1/3} \dots n^{1/(n-1)}},$$

Indefinite factorization: full pivoting

Algorithm (One step of full indefinite pivoting)

1: Set $\alpha = (1 + \sqrt{17})/8 \approx 0.64$

2: Find *a_{kk}*: diagonal entry of maximum size

3: Find a_{ij} : off-diagonal entry of maximum size (i < j)

4: if $|a_{kk}| \ge \alpha |a_{ij}|$ then

5: use a_{kk} as 1×1 pivot (ready for $a_{kk} = 0$)

6: **else**

7:	use (a_{ii}	$\begin{vmatrix} a_{ij} \\ a_{ji} \end{vmatrix}$	as 2×2 the pivot
8:	end if		55 /	

But sparsity must be also considered!

Indefinite factorization: classical scheme of symmetric partial pivoting

• The following scheme shows entries sufficient to be checked

- λ, σ : maximum absolute value in its row and column, respectively.
- That is: only two rows and columns of A searched.
- Less searches: slightly larger growth factor bound than in LU
- There are stable schemes and threshold extensions.

Solving ill-conditioned problems

• a) Preprocessing by diagonal scaling:

$$S_r A S_c y = S_r b, \qquad y = S_c^{-1} x.$$

Theorem

Let the matrix A be SPD and let D_A be the diagonal matrix with entries a_{ii} $(1 \le i \le n)$. Then for all diagonal matrices D with positive entries

$$\kappa(D_A^{-1/2} A D_A^{-1/2}) \le n z_{rmax} \kappa(D^{-1/2} A D^{-1/2}),$$

where nz_{rmax} is the maximum number of entries in a row of A.

Solving ill-conditioned problems

• a) Preprocessing by diagonal scaling:

$$S_r A S_c y = S_r b, \qquad y = S_c^{-1} x.$$

Theorem

Let the matrix A be SPD and let D_A be the diagonal matrix with entries a_{ii} $(1 \le i \le n)$. Then for all diagonal matrices D with positive entries

$$\kappa(D_A^{-1/2} A D_A^{-1/2}) \le n z_{rmax} \, \kappa(D^{-1/2} A D^{-1/2}),$$

where nz_{rmax} is the maximum number of entries in a row of A.

b) postprocessing: various iterative refinements (IR) like

Algorithm (IR of the solution x of Ax = b)

- *1:* Solve $Ax^{(0)} = b$
- 2: for k = 0, 1, ... do
- 3: Compute $r^{(k)} = b Ax^{(k)}$
- 4: Solve $A \, \delta x^{(k)} = r^{(k)}$

5:
$$x^{(k+1)} = x^{(k)} + \delta x^{(k)}$$

6: end for

- $\triangleright x^{(0)}$ is the initial computed solution
 - \triangleright Residual on iteration k
- Solve correction equation: using a factorization

Outline

Sparse Least Squares and factorizations

- Reorderings
- Algebraic preconditioning

Least squares: factorizations

- Direct methods are relevant even for LS: regular LS: normal equations: Cholesky
- Another formulation for LS:
- The normal equations are equivalent to the linear equations $A^T r = 0$, and r = b Ax that can be expressed as the $(m+n) \times (m+n)$ augmented system (z = r and c = 0). $K \begin{pmatrix} z \\ x \end{pmatrix} = \begin{pmatrix} b \\ c \end{pmatrix}$ with $K = \begin{pmatrix} I & A \\ A^T & 0 \end{pmatrix}$,

Least squares: factorizations

- Direct methods are relevant even for LS: regular LS: normal equations: Cholesky
- Another formulation for LS:
- The normal equations are equivalent to the linear equations $A^T r = 0$, and r = b Ax that can be expressed as the $(m+n) \times (m+n)$ augmented system (z = r and c = 0).

$$K\begin{pmatrix} z\\x\end{pmatrix} = \begin{pmatrix} b\\c\end{pmatrix}$$
 with $K = \begin{pmatrix} I & A\\A^T & 0 \end{pmatrix}$,

• The symmetric indefinite matrix *K* is non singular if and only if rank(A) = n: general indefinite solvers

Least squares: factorizations

- Direct methods are relevant even for LS: regular LS: normal equations: Cholesky
- Another formulation for LS:
- The normal equations are equivalent to the linear equations $A^T r = 0$, and r = b Ax that can be expressed as the $(m + n) \times (m + n)$ augmented system (z = r and c = 0).

$$K\begin{pmatrix} z\\x\end{pmatrix} = \begin{pmatrix} b\\c\end{pmatrix}$$
 with $K = \begin{pmatrix} I & A\\A^T & 0 \end{pmatrix}$,

- The symmetric indefinite matrix K is non singular if and only if rank(A) = n: general indefinite solvers
- To be more general, consider the regularized LS

$$\min_{x \in \mathbb{R}^n} (\|b - Ax\|_2^2 + \gamma^2 \|x\|_2^2) = \min_{x \in \mathbb{R}^n} \left\| \begin{pmatrix} b \\ 0 \end{pmatrix} - \begin{pmatrix} A \\ \gamma I \end{pmatrix} x \right\|_2.$$

Least squares: two solution approaches so far

- 1. SPD (Cholesky) factorization of $A^T A$
- If $\gamma > \sigma_{min}(A),$ we have $\kappa(A^TA + \gamma^2 I) \approx (\|A\|_2/\gamma)^2$
- Not a big progress since γ should be kept small.

Least squares: two solution approaches so far

- 1. SPD (Cholesky) factorization of $A^T A$
- If $\gamma > \sigma_{min}(A)$, we have $\kappa(A^TA + \gamma^2 I) \approx (\|A\|_2/\gamma)^2$
- Not a big progress since γ should be kept small.
- 2. Symmetric indefinite factorization of K

$$\begin{pmatrix} I & A \\ A^T & -\gamma^2 I \end{pmatrix} \begin{pmatrix} r \\ x \end{pmatrix} = \begin{pmatrix} b \\ 0 \end{pmatrix} \text{ or } K_{\gamma} \begin{pmatrix} s \\ x \end{pmatrix} = \begin{pmatrix} b \\ 0 \end{pmatrix}, \ K_{\gamma} = \begin{pmatrix} \gamma I & A \\ A^T & -\gamma I \end{pmatrix}, \ r = \gamma s.$$

- If $\gamma > \sigma_{min}(A)$, we have $\kappa(K_{\gamma}) \approx ||A||_2/\gamma$.
- Seems to be better, but indefiniteness.

Least squares: an additional solution approach

• Another solution strategy: using another (QR) factorization.

$$A = \left(Q_1 \ Q_2\right) \begin{pmatrix} R\\ 0 \end{pmatrix} = Q_1 R,$$

• $Q = (Q_1 \ Q_2)$ is orthogonal, $R \in \mathbb{R}^{m \times n}$ is upper triangular.

Least squares: an additional solution approach

• Another solution strategy: using another (QR) factorization.

$$A = (Q_1 \ Q_2) \begin{pmatrix} R \\ 0 \end{pmatrix} = Q_1 R,$$

- $Q = (Q_1 \ Q_2)$ is orthogonal, $R \in \mathbb{R}^{m \times n}$ is upper triangular.
- There are more ways to orthogonalize A
 - Givens rotations

- Householder reflections
- Gram-Schmidt orthogonalization
- All of them should get the same *Q* (modulo signs of the diagonal entries of *R*)
- Let us proceed to a sparse *A*, to see that the fill-in can be overestimated.

Contemporary sparse QR: symbolic phase

- Consider a symbolic phase predicting *R* or *Q*.
- A Givens rotation G(i, j) applied to $A_{i,i:n}$ and $A_{j,i:n}$ of A:

$$\begin{pmatrix} c & -s \\ s & c \end{pmatrix} \begin{pmatrix} A_{i,i:n} \\ A_{j,i:n} \end{pmatrix} = \begin{pmatrix} A'_{i,i:n} \\ A'_{j,i:n} \end{pmatrix}, \ A'_{j,i} = 0.$$

• An example that emphasizes sparsity patterns:

$$\begin{pmatrix} A_{i,i:n} \\ A_{j,i:n} \end{pmatrix} = \begin{pmatrix} * & * & * & * & * \\ * & & * & * \end{pmatrix}$$

Applying G(i, j) gives

Contemporary sparse QR: symbolic phase

• The (1, 1) entry $A'_{i,i}$ seems to remain nonzero (it is the Euclidean norm of the vector $(A_{ii} A_{ji})^T$) and the sparsity patterns of columns 2 to n satisfy

$$\mathcal{S}(A'_{i,i+1:n}) = \mathcal{S}(A_{i,i+1:n}) \cup \mathcal{S}(A_{j,i+1:n}), 1 \le i \le n-1.$$

• This is the row merge rule. Apparently, significantly more fill-in than in *LU/LL^T*

Contemporary sparse QR: symbolic phase

• However, the fill-in can be overestimated. Consider $a, b \neq 0$

$$\begin{pmatrix} * & a & b \\ * & & * & * \\ * & & * & * \end{pmatrix} \rightarrow \begin{pmatrix} * & c'ca & c'cb \\ sa & sb & * & * \\ s'ca & s'cb & * & * \end{pmatrix}$$
$$\rightarrow \begin{pmatrix} * & a & b \\ c''sa - s''s'ca & c''sb - s''s'cb & * & * \\ s''sa + c''s'ca & s''sb + c''s'cb & * & * \end{pmatrix}$$

- Steps: apply G(2,1) with c, s to eliminate the (2,1) entry; apply G(3,1) with c', s' to eliminate the (3,1) entry; eliminate the fill-in at (3,2) by rotation with c'', s''.
- We have s''sa + c''s'ca = 0.
- But this a nonzero multiple of the entry s''sb + c''s'cb at (3,3).
- The row merge rule is not able to predict that the (3,3) entry also always becomes zero.

Contemporary sparse QR: another possibility for symbolic QR

Lemma

 $S(R) \subseteq \{ \text{prediction of } S(R) \text{ based on row merge rule } \} \subseteq \{ \text{prediction of } S(R) \text{ based on } A^T A \}.$

- This surprising behavior can be suppressed by considering structural properties of *A*, this is not a problem.
- But still, the QR may not be a progress, structurally. To feel this, consider

$$A = QR \Rightarrow A^T A = R^T Q^T QR = R^T R$$

- And we have Cholesky of $A^T A$. See our concerns above.
- As for the Lemma, a practical sparse QR solver may be based on the pattern of $A^T A$.
- For example, the multifrontal method that uses $C = A^T A$ implicitly.

Contemporary sparse QR: multifrontal QR factorization


Outline

- Reorderings
- Algebraic preconditioning

Minimizing the fill-in: reorderings

• Key problem of factorizations: minimizing the fill-in. Remind:



- Our tools: symmetric permutations: $A \rightarrow PAP^T$
- Finding a permutation minimizing fill-in is NP complete: heuristics called fill-reducing orderings.
- No stability concerns: only sparsity pattern S{A} needed
- Otherwise: further permutations of *A* to force factorizability needed.

A. Local (greedy) reorderings

• Two basic greedy heuristics are the minimum degree (MD) criterion and the minimum fill (MF) criteria.

A.I. Minimum fill-in (MF) criterion

- Select as the next vertex of G(A) the one that introduce the least fill-in in G^k. Or do it approximatively (AMF).
- High quality, but the cost of MF can be prohibitive: needed to check neighbors of neighbors.

A.II. Minimum degree (MD) criterion

- Select as the next vertex a vertex of minimum degree in G^k. Or do it approximatively (AMD).
- MD is the most widely-used local heuristic. Less expensive than MF.

A. Local (greedy) reorderings: MD algorithm example



Figure: An illustration of three steps of the MD algorithm. Elimination order: \mathcal{G}^2 , \mathcal{G}^3 and \mathcal{G}^4 .

A. Local reorderings: storing and using the fill-in

• A clique with *m* vertices has m(m-1)/2 edges. This cannot be stored explicitly in the initial space!. \mathcal{G}_k must be stored implicitly.



- 4 vertices instead of 6 edges if the clique stored implicitly
- The cliques stored as lists of neighbors. As the elimination process progresses, cliques grow and can be merged.
- If vertices not merged (as blocks) $\Rightarrow \mathcal{E}_k$ (changed according to the Parter's rule) expressed as reachable sets in modified elimination graphs.

A. Local reorderings: From Parter's rule to reachable sets

• Figure: graph $\mathcal{G}(A)$. The adjacency sets of the vertices in \mathcal{G}^4 that result from eliminating vertices $\mathcal{V}^4 = \{1, 2, 3\}$ are



Figure: 1, 2, and 3 eliminated in the first three elimination steps ($\mathcal{V}^4 = \{1, 2, 3\}$).

A. Local reorderings: tricks

- The construction of \mathcal{G}^{k+1} needs some tricks to make it cheaper.
- Replication ⇒ accumulation of information: finding and exploiting analogies to the supernodes needed
- In fact, we must find supernodes without the efficient tools like the elimination tree.

A. The first acceleration trick: indistinuishability

Definition

Two different vertices \mathbf{u} and \mathbf{v} of G are called indistinguishable if



Correspond to supernodes: can be eliminated in any mutual order.

A. Second acceleration trick: degree outmatching

• Vertex w is said to be outmatched by vertex u if

$$adj_{\mathcal{G}}\{u\} \cup \{u\} \subseteq adj_{\mathcal{G}}\{w\} \cup \{w\}.$$

• It follows: $deg_{\mathcal{G}}(u) \leq deg_{\mathcal{G}}(w)$, preserved in \mathcal{G}_v for $v, v \neq u, w$



Figure: An example G in which vertex w is outmatched by vertex u.

A. Third acceleration trick: Multiple minimum degree (MMD)



• The mutually non-adjacent can be eliminated at the same time.

A. Local reorderings: complexity

- The complexity of the MD algorithms is $O(nz(A)n^2)$.
- The tricks do not change the worst-case bound.
- Additional trick: limit the search length in reachable sets: AMD (approximate minimum degree).
- The complexity of AMD is O(nz(A)n).
- In practice, runtime of AMD is typically significantly smaller than that of the MD and MMD approaches.

B. Global (nested dissection) orderings

Identify a small subset of vertices: vertex separator

Definition

Vertex separator of an undirected G = (V, E) is subset *S* of its vertices such that the subgraph induced by $V \setminus S$ has more components than *G*.

• Order it last, then the separated parts.

Induced reordering

$$A = \begin{pmatrix} A_{11} & 0 & A_{31}^T \\ 0 & A_{22} & A_{32}^T \\ A_{31} & A_{32} & A_{33} \end{pmatrix}$$

Do it recursively

(6)

Initial reordering

B. Global (nested dissection) orderings



B. Global (nested dissection) orderings



C. Minimizing fill-in + getting a favourable shape





Band

Profile (Envelope)



Frontal method : moving window determines ordering

C. Minimizing fill-in + getting a favourable shape

- Why do we do this?
- Static structures! Motivated by the following theorem:

Theorem

If L is the Cholesky factor of A then

 $envelope(A) = envelope(L), \ band(A) = band(L).$

How to get such shape? Finding a permutation!

- In advance: band, profile (envelope) methods
- On-the fly: frontal method

C. Minimizing fill-in + getting a favourable shape



C. Minimizing fill-in + getting a favourable shape



C. Minimizing fill-in + getting a favourable shape



C. Minimizing fill-in + getting a favourable shape



Complexity of (some) factorizations

- Sequential complexity dominated by the factorization, but see our comments on parallel costs
- General dense matrices
 - Space: $O(n^2)$
 - ▶ Time: *O*(*n*³)
- General sparse matrices
 - Space: $\eta(L) = n + \sum_{i=1}^{n-1} (\eta(L_{*i}) 1)$
 - ► The *i*-th step: $\eta(L_{*i}) = 1$ div, $1/2(\eta(L_{*i}) = 1)\eta(L_{*i})$ multiple-add
 - Time totally: $1/2 \sum_{i=1}^{n-1} (\eta(L_{*i}) 1)(\eta(L_{*i}) + 2)$





Complexity

Complexity

- Band schemes ($\beta << n$)
 - ► Space: O(βn)
 - Time: $O(\beta^2 n)$



Complexity of (some) factorizations

- Profile/envelope schemes
 - Space: $\sum_{i=1}^{n} \beta_i$
 - β_i : lengths of row segments containing their nonzeros
 - Complexity can expressed similarly.



Complexity of (some) factorizations

- Nested dissection
- Planar graphs, 2D finite element graphs (bounded degree)
 - Space: $O(n \log n)$
 - Time: $O(n^{3/2})$
- 3D Finite element graphs
 - ▶ Space: *O*(*n*^{4/3})
 - Time: $O(n^2)$

Outline



Algebraic preconditioning

- Finite precision fp64 arithmetic: computed factors are not exact.
- Lower precision arithmetic: even less accuracy
- Parallelism: sometimes hard to get complete factorization, the effort to obtain more accurate results can lead to complex coding and unavoidable inefficiencies: further approximation
- What about even a stronger relaxation: intentional relaxation of factorizations

Algebraic preconditioning

Two basic possibilities

• Approximate factorizing of A can be interpreted as a splitting of A

A = M - E,

Two basic possibilities

• Approximate factorizing of A can be interpreted as a splitting of A

$$A = M - E,$$

• The matrix *M* nonsingular and (easy to invert, we like to invert ©); *E* is the error matrix. The iterations are then

$$x^{(k+1)} = M^{-1}Ex^{(k)} + M^{-1}b, \quad k = 0, 1, \dots; \text{ provided } x^{(0)}$$

This can be rewritten as

stationary iterations

$$x^{(k+1)} = x^{(k)} + M^{-1}(b - Ax^{(k)}) = x^{(k)} + M^{-1}r^{(k)}, \quad k = 0, 1, \dots$$

 considered as system transformation, often used with Krylov space methods

$$x^{(approx)} = x^{(approx)} + M^{-1}(b - Ax^{(approx)}).$$

Splitting rewritten as stationary iterations

Theorem

For any initial $x^{(0)}$ and vector *b*, the stationary iteration converges if and only if the spectral radius of $(I - M^{-1}A)$ is less than unity.

• $A = D_A + L_A + U_A$: more classical choices for M

- Richardson method: $M = \omega^{-1}I$,
- ► Jacobi and damped Jacobi methods: $M = D_A$ and $M = \omega^{-1}D_A$,
- ► Gauss-Seidel and SOR methods: $M = D_A + L_A$ and $M = \omega^{-1}D_A + L_A$ ($\omega > 0$).
- Linear convergence, its guarantees.

Splitting rewritten as stationary iterations: convergence: just reminder

Theorem

If $A \in \mathbb{R}^{n \times n}$ is strongly diagonally dominant then Jacobi method and Gauss-Seidel method are convergent.

Theorem

If $A \in \mathbb{R}^{n \times n}$ is symmetric with positive diagonal D_A then the Jacobi method is convergent iff A and $2D_A - A$ are positive definite.

Theorem

If $A \in \mathbb{R}^{n \times n}$ is symmetric and positive definite then the Gauss-Seidel method is convergent.

Preconditioning as a system transformation

• Consider the preconditioned linear system

 $M^{-1}Ax = M^{-1}b.$

Here M^{-1} is applied to A from the left.

• The linear system can be also preconditioned from the right

$$AM^{-1}y = b, \qquad x = M^{-1}y.$$

Preconditioning as a system transformation

• Consider the preconditioned linear system

 $M^{-1}Ax = M^{-1}b.$

Here M^{-1} is applied to A from the left.

• The linear system can be also preconditioned from the right

$$AM^{-1}y = b, \qquad x = M^{-1}y.$$

Is one of them better? No.

Theorem

Let δ and Δ be positive numbers. Then for any $n \geq 3$ there exist nonsingular $n \times n$ matrices A and M such that all the entries of $M^{-1}A - I$ have absolute value less than δ and all the entries of $AM^{-1} - I$ have absolute values greater than Δ . Preconditioning as a system transformation

Consider the preconditioned linear system

 $M^{-1}Ax = M^{-1}b.$

Here M^{-1} is applied to A from the left.

• The linear system can be also preconditioned from the right

$$AM^{-1}y = b, \qquad x = M^{-1}y.$$

Is one of them better? No.

Theorem

Let δ and Δ be positive numbers. Then for any $n \geq 3$ there exist nonsingular $n \times n$ matrices A and M such that all the entries of $M^{-1}A - I$ have absolute value less than δ and all the entries of $AM^{-1} - I$ have absolute values greater than Δ .

- Left/right: to be compatible with the Krylov space accelerator ©
- Generally cheaper to apply M^{-1} and A separately.

From direct methods to preconditioning

• Zoological garden of approaches: structure-based, threshold-based, memory-based. Algorithms may modify the standard LU/LDL^T scheme.

From direct methods to preconditioning

- Zoological garden of approaches: structure-based, threshold-based, memory-based. Algorithms may modify the standard LU/LDL^T scheme.
- Holy grail for prescribing $\mathcal{S}(A)$?

Theorem

Consider the incomplete LU factorization $A + E = \tilde{L}\tilde{U}$ with sparsity pattern $S{\tilde{L} + \tilde{U}}$. The entries of the error matrix E are zero at positions $(i, j) \in S{\tilde{L} + \tilde{U}}$.

From direct methods to preconditioning

- Zoological garden of approaches: structure-based, threshold-based, memory-based. Algorithms may modify the standard LU/LDL^T scheme.
- Holy grail for prescribing $\mathcal{S}(A)$?

Theorem

Consider the incomplete LU factorization $A + E = \widetilde{L}\widetilde{U}$ with sparsity pattern $S{\widetilde{L} + \widetilde{U}}$. The entries of the error matrix E are zero at positions $(i, j) \in S{\widetilde{L} + \widetilde{U}}$.

- No. Improvement from an increase of $\mathcal{S}\{\widetilde{L}+\widetilde{U}\}$ are typically very slow.
- So, what to do?
From direct methods to preconditioning

- Two general theoretical directions
 - I. Avoiding breakdowns, possible for special matrices like: (M-matrix)

$$A = \begin{pmatrix} 4 & -1 & -1 & & \\ -1 & 4 & -1 & -1 & & \\ & -1 & 4 & -1 & & \\ -1 & & 4 & -1 & -1 & \\ & -1 & -1 & 4 & -1 & \\ & & -1 & -1 & 4 & -1 \\ & & & -1 & -1 & 4 & -1 \\ & & & & -1 & -1 & 4 & -1 \\ & & & & & -1 & -1 & 4 \end{pmatrix}$$

I. Avoiding breakdowns

 But factorization may breakdown even in case of Cholesky and/or low precision

$$A = \begin{pmatrix} 3 & -2 & 2 \\ -2 & 3 & -2 \\ 2 & -2 & 8 \end{pmatrix}, \ L = \begin{pmatrix} 1 \\ -2/3 & 1 \\ -6/5 & 1 \\ 2/3 & 4/5 & -2/3 & 1 \end{pmatrix}, \ D = \begin{pmatrix} 3 & 5/3 \\ & 3/5 \\ & 16/3 \end{pmatrix}$$
$$\widetilde{L} = \begin{pmatrix} 1 \\ -2/3 & 1 \\ -6/5 & 1 \\ 2/3 & -10/3 & 1 \end{pmatrix}, \ \widetilde{D} = \begin{pmatrix} 3 & 5/3 \\ & 3/5 \\ & 3/5 \\ & 0 \end{pmatrix}.$$

Algorithm (Trial-and-error global shifted incomplete factorization)

1: for $k = 0, 1, 2, \dots$ do 2: $A + \alpha^{(k)} I \approx \widetilde{L}\widetilde{U}$ 3: If successful, $\alpha = \alpha^{(k)}$ and return 4: $\alpha^{(k+1)} = 2\alpha^{(k)}$ 5: end for

> Perform incomplete factorization

II. Increasing hope for fast convergence.

- II. Such hope indicated for model problems by $\kappa(M^{-1}A)$
 - ► For example, it is possible to go from O(h⁻²) to O(h⁻¹) by special constructions and/or reorderings
 - For model problems ③
- Generally, no royal way to efficient preconditioning based on relaxed factorizations
- But, still a field with great potential for research.

Thank you for your attention!